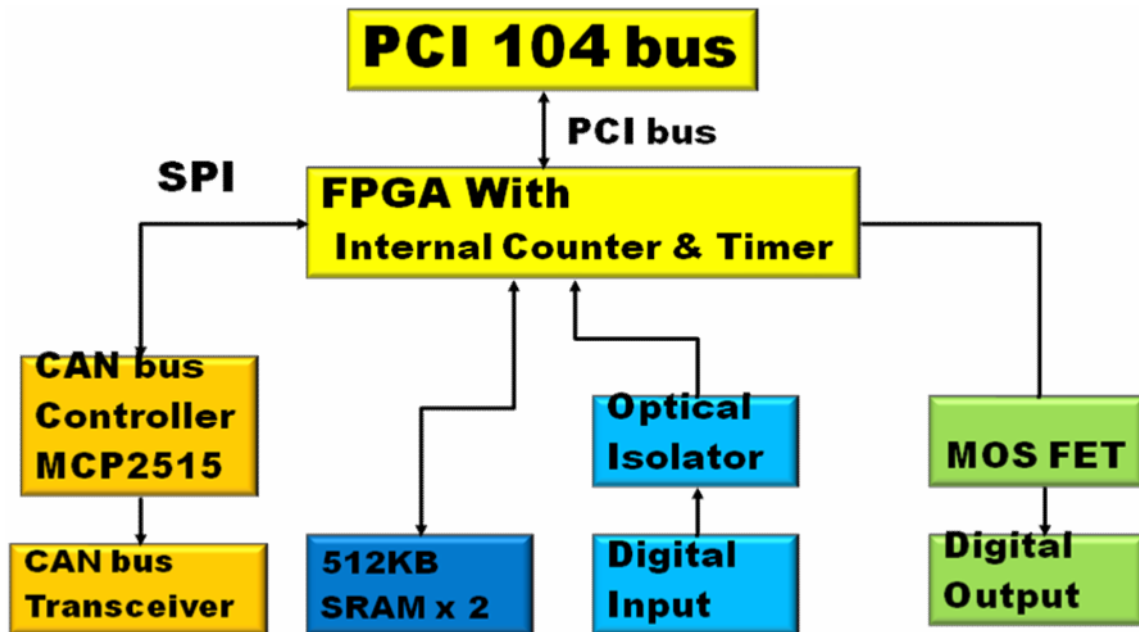


CAN bus, Digital I/O, SRAM PCI 104

Appendix 1. Block diagram



Appendix 2. PCI 104 IDSEL# and INT# table

Selection	IDSEL	INT0#	INT1#	INT3#	INT4#
0	IDSEL0	INTA#	INTB#	INTC#	INTD#
1	IDSEL1	INTB#	INTC#	INTD#	INTA#
2	IDSEL2	INTC#	INTD#	INTA#	INTB#
3	IDSEL3	INTD#	INTA#	INTB#	INTC#

Appendix 3. Digital I/O connector

PIN #	Function	PIN #	Function	PIN #	Function
1	Input 1	13	GND	25	Output 3
2	Input 2	14	GND	26	Output 4
3	Input 3	15	GND	27	Output 5
4	Input 4	16	GND	28	Output 6
5	Input 5	17	GND	29	Output 7
6	Input 6	18	GND	30	Output 8
7	Input 7	19	GND	31	Output 9
8	Input 8	20	GND	32	Output 10
9	Input 9	21	GND	33	Output 11
10	Input 10	22	GND	34	Output 12
11	Input 11	23	Output 1		
12	Input 12	24	Output 2		

Appendix 4. Digital I/O connector

PIN #	Function	PIN #	Function
1	Input 1	2	Input 2
3	Input 3	4	Input 4
5	Input 5	6	Input 6
7	Input 7	8	Input 8

9	Input 9	10	Input 10
11	Input 11	12	Input 12
13	GND	14	GND
15	GND	16	GND
17	GND	18	GND
19	GND	20	GND
21	GND	22	GND
23	Output 1	24	Output 2
25	GND	26	GND
27	Output 3	28	Output 4
29	GND	30	GND
31	Output 5	32	Output 6
33	GND	34	GND
35	Output 7	36	Output 8
37	GND	38	GND
39	Output 9	40	Output 10
41	GND	42	GND
43	Output 11	44	Output 12